81
PLUS Search Results for S/N 10602581, Searched November 16, 2004

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system for U.S. Patents from 1971 to the present. PLUS is a
query-by-example search system which produces a list of patents that a
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most closely related linguistically to the application searched. This
search was prepared by the staff of the Scientific and Technical
Information Center, SIRA.

6272601
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6272601  61
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5260881  50
5329460  50
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Most Frequently Occurring Classifications of Patents Returned
From A Search of 10602581 on November 16, 2004

Original Classifications
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7  716/16
3  710/305
2  326/39
2  326/41
2  710/306
2  710/316
2  711/140
2  711/146
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Cross-Reference Classifications
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6  326/41
6  716/17
4  326/39
4  708/232
3  340/14.3
3  710/107
2  326/38
2  326/86
2  340/2.2
2  710/309
2  711/133
2  712/19
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Combined Classifications
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8  326/41
7  716/16
7  716/17
6  326/39
4  708/232
3  326/38
3  340/14.3
3  710/107
3  710/305
3  711/146
2  326/17
2  326/83
2  326/86
2  340/2.2
2  710/240
2  710/306
2  710/309
2  710/316
2  711/133
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2   711/140
2   712/11
2   712/14
2   712/19
2   712/214
2   712/23
2   712/37
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10602581_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returne
d
From A Search of 10602581 on November 16, 2004

8 326/41   (2 OR, 6 XR)
        Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY
        326/37   MULTIFUNCTIONAL OR PROGRAMMABLE (E.G.,
                            UNIVERSAL, ETC.)
        326/39   .Array (e.g., PLA, PAL, PLD, etc.)
        326/41   ..Significant integrated structure, layout, or
                            layout interconnections

7 716/16   (7 OR, 0 XR)
        Class   716 :   DATA PROCESSING: DESIGN AND ANALYSIS OF
                            CIRCUIT OR SEMICONDUCTOR MASK
        716/1   CIRCUIT DESIGN
        716/12   .Routing (e.g., routing map, netlisting)
        716/16   ..PLA, PLD, FPGA, OR MCM

7 716/17   (1 OR, 6 XR)
        Class   716 :   DATA PROCESSING: DESIGN AND ANALYSIS OF
                            CIRCUIT OR SEMICONDUCTOR MASK
        716/1   CIRCUIT DESIGN
        716/17   .Programmable integrated circuit (e.g., basic
                            cell, standard cell, macrocell)

6 326/39   (2 OR, 4 XR)
        Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY
        326/37   MULTIFUNCTIONAL OR PROGRAMMABLE (E.G.,
                            UNIVERSAL, ETC.)
        326/39   .Array (e.g., PLA, PAL, PLD, etc.)

4 708/232   (0 OR, 4 XR)
        Class   708 :   ELECTRICAL COMPUTERS: ARITHMETIC PROCESSING
                            AND CALCULATING
        708/100   ELECTRICAL DIGITAL CALCULATING COMPUTER
        708/200   .Particular function performed
        708/230   ..Multifunctional
        708/232   ...Array of elements (e.g., AND/OR array, etc.
)

3 326/38   (1 OR, 2 XR)
        Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY
        326/37   MULTIFUNCTIONAL OR PROGRAMMABLE (E.G.,
                            UNIVERSAL, ETC.)
        326/38   .Having details of setting or programming of

3 340/14.3   (0 OR, 3 XR)
    Class   340 :   COMMUNICATIONS: ELECTRICAL
    340/825   SELECTIVE
    340/14.1   .Decoder matrix
    340/14.3   ..Programmable

3 710/107   (0 OR, 3 XR)
    Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/107   .Bus access regulation

3 710/305   (3 OR, 0 XR)
    Class   710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/305   .Bus interface architecture

3 711/146   (2 OR, 1 XR)
    Class   711 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: MEMORY
    711/100   STORAGE ACCESSING AND CONTROL
    711/117   .Hierarchical memories
    711/118   ..Caching
    711/141   ...Coherency
    711/146   ....Snooping

2 326/17   (1 OR, 1 XR)
    Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY
    326/17   ACCELERATING SWITCHING

2 326/83   (1 OR, 1 XR)
    Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY
    326/62   INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
                ETC.)
    326/82   .Current driving (e.g., fan in/out, off chip
                driving, etc.)
    326/83   ..Field-effect transistor

2 326/86   (0 OR, 2 XR)
    Class   326 :   ELECTRONIC DIGITAL LOGIC CIRCUITRY
    326/62   INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
                ETC.)
    326/82   .Current driving (e.g., fan in/out, off chip

                              driving, etc.)
    326/83   ..Field-effect transistor
    326/86   ...Bus driving

2 340/2.2   (0 OR, 2 XR)
    Class   340 : COMMUNICATIONS: ELECTRICAL
    340/825   SELECTIVE
    340/2.1   .Path selection
    340/2.2   ..Channel selecting matrix

2 710/240   (1 OR, 1 XR)
    Class   710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/240   ACCESS ARBITRATING

2 710/306   (2 OR, 0 XR)
    Class   710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/305   .Bus interface architecture
    710/306   ..Bus bridge

2 710/309   (0 OR, 2 XR)
    Class   710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/305   .Bus interface architecture
    710/306   ..Bus bridge
    710/309   ...Arbitration

2 710/316   (2 OR, 0 XR)
    Class   710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
                PROCESSING SYSTEMS: INPUT/OUTPUT
    710/100   INTRASYSTEM CONNECTION (E.G., BUS AND BUS
                TRANSACTION PROCESSING)
    710/305   .Bus interface architecture
    710/316   ..Path selecting switch

2 711/133   (0 OR, 2 XR)
    Class   711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                SYSTEMS: MEMORY
    711/100   STORAGE ACCESSING AND CONTROL
    711/117   .Hierarchical memories
    711/118   ..Caching
    711/133   ...Entry replacement strategy

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  2  711/140       (2 OR,  0 XR)
        Class    711 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS: MEMORY
        711/100          STORAGE ACCESSING AND CONTROL
        711/117          .Hierarchical memories
        711/118          ..Caching
        711/140          ...Cache pipelining


  2  712/11        (1 OR,  1 XR)
        Class    712 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                           PROCESSING
        712/1            PROCESSING ARCHITECTURE
        712/10           .Array processor
        712/11           ..Array processor element interconnection


  2  712/14        (1 OR,  1 XR)
        Class    712 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                           PROCESSING
        712/1            PROCESSING ARCHITECTURE
        712/10           .Array processor
        712/11           ..Array processor element interconnection
        712/14           ...Processing element memory


  2  712/19        (0 OR,  2 XR)
        Class    712 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                           PROCESSING
        712/1            PROCESSING ARCHITECTURE
        712/10           .Array processor
        712/16           ..Array processor operation
        712/19           ...Systolic array processor


  2  712/214       (1 OR,  1 XR)
        Class    712 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                           PROCESSING
        712/214          INSTRUCTION ISSUING


  2  712/23        (1 OR,  1 XR)
        Class    712 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                           SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
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                            10602581_CLSTITLES
                            PROCESSING
            712/1           PROCESSING ARCHITECTURE
            712/23          .Superscalar

    2   712/37          (1 OR, 1 XR)
            Class   712 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
                            SYSTEMS:  PROCESSING ARCHITECTURES AND    INS
TRUCTION
                            PROCESSING
            712/1           PROCESSING ARCHITECTURE
            712/32          .Microprocessor or multichip or multimodule
                                processor having sequential program contro
    l
            712/37          ..Programmable (e.g., EPROM)
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